

# **Inverter increases instantaneous output power**





## Overview

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Can CMOS inverters reduce power consumption?

The problem of reducing power consumption is especially relevant in microprocessors, high-speed ICs, wearable devices, and devices with powerful output buffers. Therefore, in digital circuits, reducing the power consumption of CMOS inverters is an actual problem.

What are the characteristics of inverters?

Another important characteristic of these resources is asynchronicity, the result of using inverters to interface the prime energy source with the power system as opposed to synchronous generators.

Why do we need multilevel inverters?

Contemporary times are critical for power electronics-based conversion systems, as they facilitate the dependable and efficient utilization of renewable energy sources [1]. Multiple decades have been devoted to investigating and advancing multilevel inverters (MLIs).

How to reduce short-circuit current and dynamic power consumption of CMOS inverter?

Therefore, the purpose of this work is to reduce short-circuit current and dynamic power consumption of the CMOS inverter. For this purpose, it is proposed to limit the short-circuit current by changing the state of additional PMOS and NMOS transistors included in the path of the short-circuit current.



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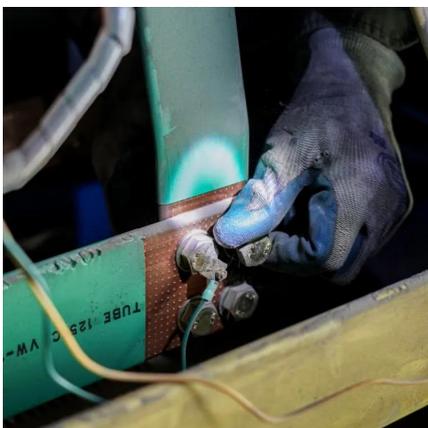
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